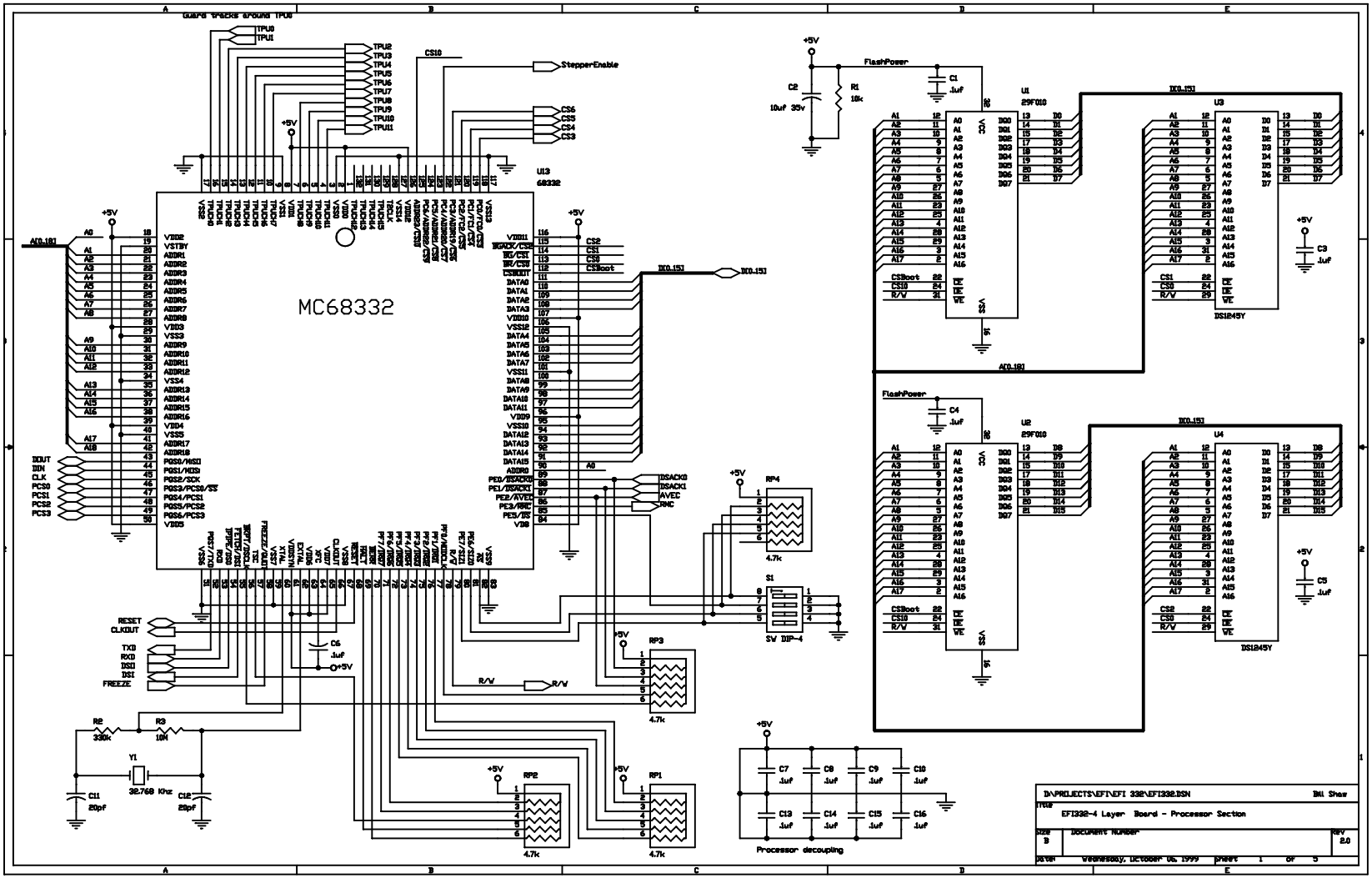
Guard tracks around TPU6
MC68332
U13
68332
TPU0
TPU1
TPU2
TPU3
TPU4
TPU5
TPU6
TPU7
TPU8
TPU9
TPU10
TPU11
CS10
StepperEnable
CS6
CS5
CS4
CS3
+5V
A0-19
D0-15
A0
A1
A2
A3
A4
A5
A6
A7
A8
A9
A10
A11
A12
A13
A14
A15
A16
A17
A18
ADDR1
ADDR2
ADDR3
ADDR4
ADDR5
ADDR6
ADDR7
ADDR8
ADDR9
ADDR10
ADDR11
ADDR12
ADDR13
ADDR14
ADDR15
ADDR16
ADDR17
ADDR18
VDD2
VSTBY
VDD3
VSS3
VSS4
VDD4
VSS5
VDD5
VDD11
VSS12
VSS11
VDD9
VSS10
VDD10
DATA0
DATA1
DATA2
DATA3
DATA4
DATA5
DATA6
DATA7
DATA8
DATA9
DATA10
DATA11
DATA12
DATA13
DATA14
DATA15
ADDR0
IRQ/CS8
BG/CS7
BR/CS9
CSBOOT
CS0
CS1
CS2
CSBoot
PE0/DSACK0
PE1/DSACK1
PE2/AVEC
PE3/RMC
PE5/IS
VDD8
DSACK0
DSACK1
AVEC
RMC
MODCLK
BKPT/DSCLK
FREEZE/IFETCH
IPIPE/IWR
IFETCH/IRD
TSC
VFLASH
EXTAL
XTAL
VDDSYN
VDD
CLKOUT
VSS
RESET
HALT
PF7/IRQ7
PF6/IRQ6
PF5/IRQ5
PF4/IRQ4
PF3/IRQ3
PF2/IRQ2
PF1/IRQ1
PF0/IRQ0
PF7/TIN
PF6/TOUT
PC7
PC6
PC5
PC4
PC3/MISO
PC2/MOSI
PC1/SCK
PC0/SS
XFC
VSSPLL
VDDPLL
RESET
CLKOUT
TXD
RXD
DSO
DSI
FREEZE
R/W
C5
.1uf
C6
.1uf
R2
330k
R3
10M
Y1
32.768 Khz
C11
20pF
C12
20pF
RP1
RP2
RP3
RP4
4.7k
S1
SW DIP-4
FlashPower
C1
.1uf
C2
10uf 35V
R1
10k
U1
29F010
U2
29F010
U3
DS1245Y
U4
DS1245Y
C3
.1uf
C4
.1uf
C7
.1uf
C8
.1uf
C9
.1uf
C10
.1uf
C13
.1uf
C14
.1uf
C15
.1uf
C16
.1uf
Processor decoupling
D:\PROJECTS\EFI\EFI 332\EFI332.DSN
Bill Shaw
EFI332-4 Layer Board - Processor Section
Size
B
Document Number
Rev
2.0
Date: Wednesday, October 06, 1999
Sheet 1 of 5